# Lect. 19: Switched Capacitor Filters (S&S 12.10)

Large resistors in active RC filters are NOT practical in IC

- Large area
- Accurate control of R very difficult

Circuit technique to solve the problem

➔ Replace resistors with capacitors: Switched capacitor







Assume initially  $\phi_2$  ON,  $\phi_1$  OFF and  $V_1 > V_2$  $Q = CV_2$ When  $\phi_2$  is OFF and  $\phi_1$  is ON Q=CV<sub>1</sub>  $\Delta Q = C(V_1 - V_2)$  supplied by  $V_1$ When  $\phi_1$  is OFF and  $\phi_2$  is ON  $Q=CV_2$  $\Delta Q = C(V_1 - V_2)$  supplied to  $V_2$  $\rightarrow$  Switched capacitors deliver charges from V<sub>1</sub> to V<sub>2</sub>  $\Delta Q = C(V_1 - V_2)$  during T<sub>c</sub>  $\frac{V_1 - V_2}{i_{arr}} = \frac{T_c}{C} = R_{eq}$  $=\frac{C(V_1-V_2)}{T_2}$ 

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(c)

*i*<sub>av</sub>



 $\phi_1$  $\phi_2$  $\leftarrow T_c$ (c) Switches are usually realized with MOS switches having finite R, C

In this course, assume ideal switches

If  $1/T_c >>$  frequency of interests, SW is acting as a resistor.



Switched Capacitor Amplifier



Initially, assume  $Q_1$ ,  $Q_2 = 0$ 

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When \phi_1 is ON, \phi_2 is OFF

Q_1 = C_1 V_i

Q_2 = C_2 (0 - V_0)

But Q_1 = Q_2

C_1 V_i = -C_2 V_0

\therefore V_0 = -\frac{C_1}{C_1} V_i
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Inverting Amplifier  $\rightarrow$  Gain determined by the ratio of C's



Switched Capacitor Amplifier



During  $\phi_1$ 

$$Q_1 = C_1 V_i \qquad Q_2 = 0$$

During  $\phi_2$  $Q_1 = 0$   $Q_2 = C_2 (V_0 - 0)$ 

But  $Q_1 = Q_2$ 

$$C_1 V_i = C_2 V_o$$
$$\therefore V_o = \frac{C_1}{C_2} V_i$$





Non-inverting weighted summer

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Non-inverting weighted summer

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Any active RC filter (such as biquad) can be replaced with equivalent SC filter  $\rightarrow$  Project #3

SC filters perform discrete time domain signal processing

s-domain analysis for continuous time signal processing

➔ z-domain analysis for discrete time signal processing

However, SC circuits have speed limitations